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the CPU architectures: i.
CISC and RISC: CISC: Complex
Instruction Set Computer In
the early days of the
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of the program development

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Paper was done in assembly language because high level languages were not developed. Therefore CPU designers tried to make instructions ...

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its developers and stands
for Alf-Egil Bogen Vegard
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microcontroller, also known
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An AVR microcontroller is a type of device manufactured by Atmel, which has particular benefits over other common chips, but first what is a microcontroller? The easiest

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Way of thinking about it is to compare a microcontroller with your PC, which has a motherboard in it. On that motherboard is a microprocessor (Intel, AMD chips) that provides the intelligence, RAM and EEPROM

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memories and ...

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towards automation rapidly in each and every fields. The students studying the subject are supposed to learn the architecture and programming of typical microcontroller. Students will be taught the basic use

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of an assembly as well as
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of fuse bits for the AVR microcontrollers. It must be noted that if a fuse bit is incor-rectly programmed, it can cause the system to fail. An example of this is changing the SPIEN bit to 1, which disables SPI

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programming mode. In this case you will not be able to program the chip any more! Also notice that the fuse bits are '0' if they are programmed and '1' when they are not ...

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completely regular: Pointer registers X, Y, and Z have addressing capabilities that are different from each other.; Register locations R0 to R15 have more limited addressing capabilities than register ...

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