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Design of CMOS Phase-Locked Loops

Behzad Razavi exposes the fundamental issues of frequency planning in transceiver design, as well as recent trends in highly integrated RF circuit design, in the context of a GSM/DCS dual-mode ...

Chapter 6: RF Front-End Circuits

This chapter describes the design of a CMOS transceiver targeting dual-band applications in the 900 MHz and 1.8 GHz bands. The receiver is based on a modified Weaver architecture, and the transmitter ...

Chapter 8: Design Study of A 900 MHz/1.8 GHz CMOS Transceiver For Dual-Band Applications

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COMP_ENG 393, 493: Advanced Low Power Digital and Mixed-signal Integrated Circuit Design

3). The key difference is where the sampling operation takes place. In the continuous-time design, input sampling takes place just before the quantizer. The loop filter is now continuous-time ...

What's The Difference Between Continuous-Time And Discrete-Time Delta-Sigma ADCs?

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