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forward bias:  $v > 0$  reverse  
bias:  $v < 0$  breakdown:  $v \ll$   
0 discontinuity caused by  
differences in scale

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6 Chapter 7 Problem: 7.96 1.  
The schematic for this

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problem is shown below 2.

The transistor used here has  $k_n' = 71.2 \mu\text{A}/\text{V}^2$ . So,  $W/L = 14\mu/0.5\mu$  is chosen to get  $k_n = 2 \text{ mA}/\text{V}^2$ . 3. Simulate the netlist and find out the operating voltages. 4. The other operating parameters

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2 Chapter 12 4. The cross over interval is  $2 \times 2.9$   
 $\mu\text{s} = 5.8 \mu\text{s}$ . So, it is 5.8 %.

5. Run the parametric analysis and sweep RL from 500? to 700? in steps of 50? or smaller. Plot V(V0) as

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shown below. 6. The output voltage is half of the input voltage when  $R_L = 650 \text{ } \Omega$ .

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