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Lecture 1

Understanding

Logic

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What is LEC?

*Conformal Field
Theory (CFT)*

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Transformations

S-7 | Logic

*Equivalence Check
using Formality |*

RTL-to-GDSII flow |

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Lemma and

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Riemann Mappings*

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Physics (NEW~~

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1/7] **Solving the**~~

**Impossible in
Quantum Field
Theory | Space**

Time The First
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~~Time Quantum
field theory,
Lecture 3 Quantum
field theory,
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Correspondence,
Part 1 - Juan
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~~Complex Mappings~~

~~Unit I: Lec 2 | MIT~~

~~Calculus Revisited:~~

~~Single Variable~~

~~Calculus Part III:~~

~~Linear Algebra, Lec~~

~~1: Vector Spaces~~

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19: Analtical

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Part II: Differential

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*Equations, Lec 1:
The Concept of a
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how to formally
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synthesized design

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is functionally equivalent to the RTL description using LEC. This section can be skipped if one chooses to use Formality for equivalence checking. Digital Logic ...

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Section 4 of this tutorial describes how to formally verify that the synthesized design is functionally equivalent to the RTL description using LEC. This section can be skipped if one chooses to use

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