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When  $J=1$ , and  $K=0$ , the output  $Q=1$  is passed to the J input of the slave. The clock's negative transition sets the slave and copies the master. The master flip flop toggles on the clock's positive transition when the inputs J and K set to 1. At that time, the slave flip flop toggles on the clock's negative transition.

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The first circuit uses an analogue sensing device, a phototransistor connected in series with a  $50k \square$  resistor, to make a light-sensing unit. The output voltage is measured using a DMM (digital multime- ter.) Set up this circuit as shown opposite. Set the DC power supply to 6V, and switch it on. Set the DMM to read voltages up to 20V DC.

An introduction to digital electronics

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Electronics, G. J. University, Hisar, for the healthy discussions on the subject. The author gratefully acknowledges the motivation from all colleagues and friends with special reference to Shri.

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