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Digital Systems Design Using VHDL
This software has several limitations including restrictions on file sizes and design names, but it is a full implementation of the IEEE standard VHDL and it has a very friendly user interface. The Aldec Active-HDL Student Edition is also available packaged with Digital Systems Design Using VHDL from Brooks/Cole. All of the examples in the book should compile and simulate correctly using Active-HDL version 3.5 Student Edition, with the exception of the 6805 microcontroller example in ...

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Digital System Design with VHDL eBook: Zwolinski, Mark –
VHDL stands for very high-speed integrated circuit hardware description language. It is a programming language used to model a digital system by dataflow, behavioral and structural style of modeling. This language was first introduced in 1981 for the department of Defense (DoD) under the VHSIC program.

VLSI Design – VHDL Introduction – Tutorialspoint
Digital systems design with VHDL is a great book from the VHDL perspective. As the other reviewer said, it’s presented in a hardware perspective rather than language features, but I would say that some chapters tend to be language features oriented.

Digital System Design with VHDL (2nd Edition): Zwolinski –
Since the publication of the first edition, a new version of the VHDL standard has been agreed and analogue extensions to the language have also been adopted. The second edition of Digital System Design with VHDL includes additions in two important areas; sections on writing testbenches have been added to relevant chapters, and the addition of a new chapter on VHDL-AMS and mixed-signal modeling.

Zwolinski, Digital System Design with VHDL | Pearson
Today digital designers use hardware description languages (HDLs) to design digital systems. The most widely used HDLs are VHDL and Verilog. Both of these hardware description languages allow the user to design digital systems by writing a program that describes the behavior of the digital circuit. The program can then be used

Introduction to Digital Design Using Digilent FPGA Boards
Digital systems design with VHDL is a great book from the VHDL perspective. As the other reviewer said, it’s presented in a hardware perspective rather than language features, but I would say that some chapters tend to be language features oriented.

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