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16 mark 1. Define a D to Gray code converter. Use dont cares 2. Design a half adder and full adder circuits 3. Design a half subtractor and design a subtractor 4. Design a 4-bit binary to BCD code converter 5. Construct a 4-bit binary to gray code converter circuit and discuss its operation 6.

DPSD-IMPORTANT-16-MARKS-AND-2-MARKS-QUESTIONS-UNITWISE

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CS8351-Digital-Principles-and-System-Design-important----

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Q46. 7BF 16 = _____. 2. 0111 1011 1110; 0111 1011 1111; 0111 1011 0111; 0111 1011 0011; Answer : 2. Explanation: 7BF 16 = 7 x 16 2 + 11 x 16 1 + 15 x 16 0 = 1983 in decimal = 0111 1011 1111 in binary. Q47. For the minterm designation Y = $\sum m(1, 3, 5, 7)$ the complete expression is _____. Answer : 2. Explanation: Q48.

Multiple-Choice-Questions-and-Answers-on-Digital----

(16) 2. Describe the operation of SR flip-flop (16) 3. Design a sequential circuit using JK flip-flop for the following state table [use state diagram] (16) 4. The count has a repeated sequence of six states, with flip flops B and C repeating the binary count 00, 01, 10 while flip flop A alternates between 0 and 1 every three counts.

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