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~~Si= Ci + Ai+ Bi, Ci+1= Ci(Ai + Bi) + AiBi. VLSI Test Principles and Architectures Ch. 2 - Design for Testability - P. 3/12. 2.5 (Ad Hoc Technique) Fig. 7: An example of a combinational feedback loop in a combinational circuit. Figure 7 shows an example of a combinational feedback loop. In this example, when A = 1, B = 0, and C = 1, the circuit acts as an oscillator, which prevents us from being able to predict a valid value at G.~~

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