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~~Investigates new topics including continuous-time $\Delta\Sigma$ analog-to-digital converters (ADCs) principles and designs, circuit design for both continuous-time and discrete-time $\Delta\Sigma$ ADCs, decimation and interpolation filters, and incremental ADCs Provides emphasis on practical design issues for industry professionals Wiley; December 2016~~

Understanding Delta-Sigma Data Converters Richard Schreier Analog Devices, Inc. Gabor C. Temes Oregon State University
OIEEE IEEE Press iWILEY-INTERSCIENCE A JOHN WILEY & SONS, INC., PUBLICATION. Contents CHAPTER 1 Foreword xi
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Delta-sigma ($\Delta\Sigma$; or sigma-delta, $\Sigma\Delta$) modulation is a method for encoding analog signals into digital signals as found in an analog-to-digital converter (ADC).

Delta-sigma modulation - Wikipedia

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The oversampled family of converters, to which the Delta-Sigma ADC belongs, aims to overcome the limitations of Nyquist-rate converters. The Delta-Sigma ADC consists of a modulator, a filter, and a decimator as shown below. Delta-Sigma ADCs are approximately 75% digital.

Understanding the Delta-Sigma ADC - Technical Articles

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