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The verilog is a bit out of date although he author does explain that newer versions have some differences. The first chapter on IN and OUT is pretty good as an introduction, several examples of combinatorial logic (although not really developing useful example).

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Verilog, standardized as IEEE 1364, is a hardware description language (HDL) used to model electronic systems. It is most commonly used in the design and verification of digital circuits at the register-transfer level of abstraction. It is also used in the verification of analog circuits and mixed-signal circuits, as well as in the design of genetic circuits.