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He has published over 650 papers, is inventor or coinventor of 31 issued US Patents, has authored the text book VLSI Digital Signal Processing Systems: Design and Implementation (Wiley, 1999), and is the co-editor (with Takao Nishitani) of the reference book Digital Signal Processing for Multimedia Systems (CRC Press, March 1999).

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1. VLSI Digital Signal Processing Systems, Keshab K. Parhi, Wiley Eastern
2. Digital Signal Processing for Multimedia Systems, Keshab K. Parhi and Takao Nishitani, Marcel Dekker.
3. Pipelined Lattice and Wave Digital Recursive Filters, J. G. Chung and Keshab K. Parhi, Kluwer.

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Pipelining (DSP implementation) - Wikipedia

VLSI digital signal processing systems: design and implementation. KK Parhi. John Wiley & Sons, 1999. 2380 * 1999: High-speed VLSI architectures for the AES algorithm. X Zhang, KK Parhi. IEEE transactions on very large scale integration (VLSI) systems 12 (9), 957-967, 2004. 517: 2004:

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